

FIG. 4

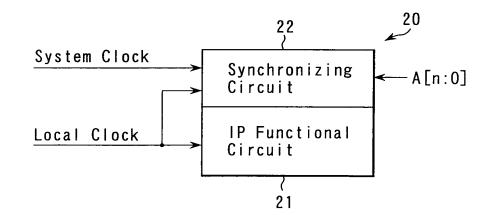
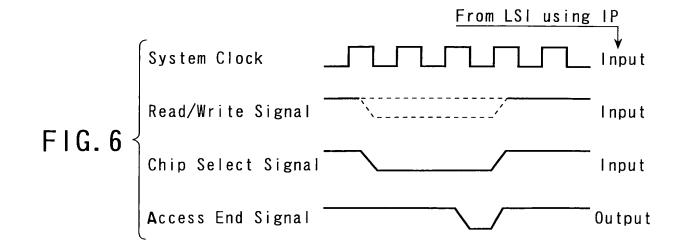


FIG. 5



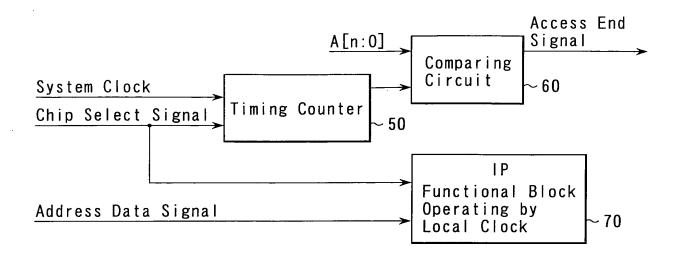


FIG. 7

$$\frac{000}{20\text{MHz}} \xrightarrow{001} 40\text{MHz} \xrightarrow{010} 60\text{MHz} \xrightarrow{011} 80\text{MHz} \xrightarrow{100} 100\text{MHz}$$
System Clock

FIG. 8